



Shri Vaishnav Vidyapeeth Vishwavidyalaya
Shri Vaishnav Institute of Technology and Science
Choice Based Credit System (CBCS) Scheme in the light of NEP-2020
B.Tech. in Electronics and Instrumentation

SEMESTER-III (2021-2025)

S. No.	COURSE CODE	COURSE NAME	TEACHING SCHEME/WEEK			CREDITS	EXAMINATION SCHEME					TOTAL MARKS
			L	T	P		THEORY			PRACTICAL		
							End Sem University Exam (60%)	Two Term Exam (20%)	Teachers Assessment* (20%)	End Sem University Exam (60%)	Teachers Assessment* (40%)	
1	BTEC303	Electronic Measurement and Instrumentation	3	0	2	4	60	20	20	30	20	150
2	BTEC302	Network Analysis and Synthesis	3	1	2	5	60	20	20	30	20	150
3	BTEC304	Electronic Devices and Circuits	3	1	2	5	60	20	20	30	20	150
4	BTECIOT401	Sensors and Signal Conditioning	3	0	2	4	60	20	20	30	20	150
5	BTMT302	PCB Designing Lab	0	0	4	2	0	0	0	30	20	50
6	-	Generic Elective I	3	0	0	3	60	20	20	0	0	100
TOTAL			15	2	12	23	300	100	100	150	100	750

Legends: L - Lecture; T - Tutorial/Teacher Guided Student Activity; P - Practical;

*Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.

Chairperson
Board of Studies
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore

Chairperson
Faculty of Studies
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore

Controller of Examinations
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore

Joint Registrar
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore

OK

29/3/2022

Vice Chancellor
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore